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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Luetzen, *et al.* Docket No.: INF115
Serial No: 10/721,225 Art Unit: 1763
Date Filed: November 26, 2003
Title: Method and Structures for Increasing the Structure Density and the Storage Capacitance in a Semiconductor Wafer

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- Certification of Facsimile Transmission (1 page)
- Information Disclosure Statement (1 page)
- PTO/SB/08A with 1 reference cited (1 page)

Respectfully submitted,



Kristin R. Hayes
Legal Assistant

Confirmation Respectfully Requested

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Mail Stop Amendment
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INFORMATION DISCLOSURE STATEMENT

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08A that may be considered material to the examination of the above-identified application. A copy of the U.S. Patent cited is not being submitted.

No fee is due at this time, as this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(b)(3), before the mailing of a first Office action on the merits.

Respectfully submitted,

February 22, 2006
Date


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| Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i> | | | | Complete if Known | |
| | | | | Application Number | 10/721,225 |
| | | | | Filing Date | November 28, 2003 |
| | | | | First Named Inventor | Luetzen, et al. |
| | | | | Art Unit | 1763 |
| | | | | Examiner Name | Goudreau, George A. |
| | | | | Attorney Docket Number | INF115 |
| Sheet | 1 | of | 1 | | |